

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination EDELEN ET AL.	
		Examiner	Art Unit	Page 1 of 1
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	C	US-2002/0060333 A1	05-2002	Tanaka et al.	257/299
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	M	US-			

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NON-PATENT DOCUMENTS

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	U	Stanley Wolf, Silicon Processing for the VLSI Era, 1990, Lattice Press, Volume 2, pp.354-356
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.